

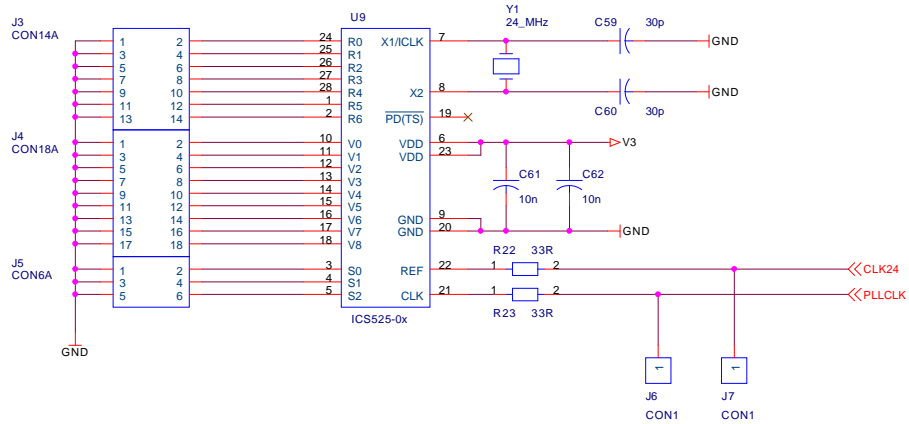
CY7C68013-128AC

CY7C1019V33-12VC
128K x 8 SRAM

CESYS GmbH

URL: <http://www.cesys.com>
Email: sales@cesys.com
M. Kraus

Title		
USB 2.0 Controller		
Size	Document Number	Rev
A3	C1030_1202 (USB2FPGA)	0.1
Date:	Thursday, December 05, 2002	Sheet 1 of 3



CESYS GmbH

www.cesys.com
 Email: sales@cesys.com
 Herzogenaurach, Germany

Title		
PLL Clock Oscillator		
Size	Document Number	Rev
A3	C1030_1202 (USB2FPGA)	1.1
Date:	Thursday, December 05, 2002	Sheet 3 of 3